

# Classical linear codes

## Classical linear codes: Generator matrix

- Linear code  $C: [n, k]$  code
  - Encoding  $k$  bits of information into an  $n$  bit code space
  - Described by  $n \times k$  generator matrix  $G$ 
    - Entries  $\in \mathbb{Z}_2 = \{0, 1\}$
  - $k$ -bit message  $x \rightarrow Gx$
- Advantage of linear code
  - Compact specification
  - $[n, k]$ :  $kn$  bits of general matrix
  - General encoding requires  $n2^k$  bits
    - Exponential saving

## Examples

- $[n, k]$  code
  - Generator matrix  $G (n \times k)$
  - $k$ -bit message  $x \rightarrow Gx$
- $[3, 1]$  code
  - $0 \rightarrow 000, 1 \rightarrow 111$      $G = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix} \Rightarrow G[0] = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, G[1] = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix}$
- $[6, 2]$  code
  - $00 \rightarrow 000000, 01 \rightarrow 000111, \dots$
  - $G = \begin{bmatrix} 1 & 0 \\ 1 & 0 \\ 1 & 0 \\ 0 & 1 \\ 0 & 1 \\ 0 & 1 \end{bmatrix} \Rightarrow G[0] = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix}, G[1] = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \\ 1 \\ 1 \end{bmatrix}, G[2] = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \\ 1 \\ 1 \end{bmatrix}, G[3] = \begin{bmatrix} 1 \\ 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}, G[4] = \begin{bmatrix} 1 \\ 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}, G[5] = \begin{bmatrix} 1 \\ 1 \\ 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}$

## Parity check matrix $H$

- $[n, k]$  code: Parity check matrix  $H$ 
  - $(n-k) \times n$  matrix
  - All elements are 0, or 1 ( $\mathbb{Z}_2$ )
- $G \rightarrow H$ 
  - Find  $n-k$  linearly independent vectors  $y_i$  orthogonal to the columns of  $G$
  - Set the rows of  $H$  be  $y_1^T, y_2^T, \dots, y_{n-k}^T$
- $H \rightarrow G$ 
  - Find  $k$  linearly independent vectors  $z_i$  orthogonal to the rows of  $H$
  - Set the columns of  $G$  be  $z_1, z_2, \dots, z_k$
- $HG = 0$

## Parity check matrix for $[3, 1]$ code

- $[3, 1]$  code
  - $G = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix} \Rightarrow G[0] = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, G[1] = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix}$      $n \times \begin{Bmatrix} k \\ G \end{Bmatrix}$
  - To construct  $H$ , pick  $3-1=2$  linearly independent vectors orthogonal to  $G$ 
    - $H = \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \end{bmatrix}$      $n-k \times \begin{Bmatrix} n \\ H \end{Bmatrix}$
  - $HG = 0$
  - Parity check
    - $Hx = 0$  only for  $x = G[0] = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, G[1] = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix} \Rightarrow$  Error detection

## Error detection by parity check

- How error correction works
  - Encode message  $x$  as  $y = Gx$
  - Error  $\rightarrow y' = y \oplus e$
  - Error syndrome:  $Hy' = H(y+e) = He$  ( $Hy = 0$ )
    - No error:  $Hy' = 0$
    - Error in qubit  $j$ :  $He_j$ 
      - $e_j$ : unit vector with 1 in the  $j$ -th component
- $[3, 1]$  code
  - Error syndrome for bit flip on bit 1, 2 & 3
    - $H \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix} = H \begin{bmatrix} 0 \\ 1 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 1 \end{bmatrix}, H \begin{bmatrix} 0 \\ 1 \\ 0 \end{bmatrix} = H \begin{bmatrix} 1 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix}, H \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = H \begin{bmatrix} 1 \\ 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$

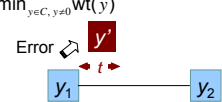
### Syndrome measurements $\equiv$ Parity check

- [3,1] code (3-qubit bit flip code)
  - “Syndrome” measurements
    1.  $Z_1 Z_2 (= Z \otimes Z \otimes I) = \begin{cases} +1 & \text{if qubit 1 = qubit 2} \\ -1 & \text{if qubit 1} \neq \text{qubit 2} \end{cases}$   
(Bit flip on one of the bits)
    2.  $Z_2 Z_3 (= Z \otimes I \otimes Z) = \begin{cases} +1 & \text{if qubit 2 = qubit 3} \\ -1 & \text{if qubit 2} \neq \text{qubit 3} \end{cases}$   
(Bit flip on one of the bits)
  - Parity check
 
$$H \equiv \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \end{bmatrix}$$

$$H \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix} = H \begin{bmatrix} 0 \\ 1 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix}, H \begin{bmatrix} 0 \\ 1 \\ 0 \end{bmatrix} = H \begin{bmatrix} 1 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 1 \end{bmatrix}, H \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = H \begin{bmatrix} 1 \\ 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

### Global properties of the code

- Hamming distance  $d(x,y)$ 
  - Distance between words  $x$  and  $b$
  - $\equiv$  number of places at which  $x$  and  $y$  differ
  - $d((1,1,0,0),(0,1,0,1)) = 2$
- Hamming weight  $wt(x) = d(x,0)$ 
  - $wt(x+y) = d(x,y)$
- Connection to error correction
  - $y = Gx \rightarrow y' = y + e$
  - Code: Replace  $y'$  by  $y$  such that  $wt(e) = d(y,y')$  is minimized
- Distance of a code  $C$ 
  - Minimum distance between any two codewords
  - $d(C) \equiv \min_{y_1, y_2 \in C, y_1 \neq y_2} d(y_1, y_2) = \min_{y \in C, y \neq 0} wt(y)$
  - $C: [n, k, d]$  code
  - $d \equiv d(C) = 2t + 1$
  - $\rightarrow$  Correct errors on up to  $t$  bit



### When error correction fails

- If  $He_1 = He_2$  for  $e_1 \neq e_2$  (weight  $\leq t$ )
  - Same syndrome for different errors
  - When  $e_1$  occurs:  $v \rightarrow v + e_1$ 
    - Faulty error recovery: apply  $e_2$
    - $v \rightarrow v + e_1 + e_2 \neq v$
  - Message after error recovery
 
$$H(v + e_1 + e_2) = 0$$

$$\Rightarrow e_1 + e_2 \in C \text{ (code subspace)}$$
  - Weight of  $e_1 + e_2 \leq 2t$
  - If distance of the code  $C$ ,  $d(C) = 2t + 1$ , then  $e_1 + e_2$  the code cannot be in  $C$
  - $\rightarrow He_1 \neq He_2$
- $\rightarrow$  Code  $C$  with distance  $d = 2t + 1$  can correct errors with weight  $\leq t$

### Examples

- $[n, k, d]$  code  $C: d \equiv d(C) = 2t + 1 \rightarrow$  Correct errors on up to  $t$  bit
- Distance  $d =$  Minimum distance between any two codewords
- $d(C) \equiv \min_{y_1, y_2 \in C, y_1 \neq y_2} d(y_1, y_2) = \min_{y \in C, y \neq 0} wt(y)$
- [3,1] code
  - $G = \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 1 \end{bmatrix} \Rightarrow G[0] = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, G[1] = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix}$
- [6,2] code
  - Code subspace
  - $C = \left\{ \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix}, \begin{bmatrix} 0 \\ 0 \\ 1 \\ 1 \\ 0 \\ 0 \end{bmatrix}, \begin{bmatrix} 1 \\ 1 \\ 0 \\ 0 \\ 1 \\ 1 \end{bmatrix}, \begin{bmatrix} 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \end{bmatrix} \right\}$
- Code subspace
  - $C = \left\{ \begin{bmatrix} 0 \\ 0 \\ 1 \\ 1 \end{bmatrix}, \begin{bmatrix} 1 \\ 1 \\ 0 \\ 1 \end{bmatrix} \right\}$

Distance  $d(C) = 3 \rightarrow$  can correct 1 error

### Hamming code

- Parity check matrix  $[n, k]$ 
  - $n = 2^r - 1, k = n - r, r \geq 3$
- [7,4,3] code: ( $r = 3$ )
  - $H \equiv \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 \end{bmatrix}$
  - $G = \begin{bmatrix} 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 1 \\ 1 & 1 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 \\ 0 & 1 & 1 & 0 \\ 1 & 1 & 1 & 0 \end{bmatrix}$
  - All columns are linearly independent.
  - Error syndrome  $He_j$
  - Distance  $d(C) = 3$

### CSS (Calderbank-Shor-Steane) code – Quantum error correcting code

### Construction of CSS code

- Subcode
  - Classical linear code:  $C_1 [n, k_1]$ 
    - $(n-k_1) \times n$  Parity check matrix  $H_1$
  - $C_2 [n, k_2]$ : subcode of  $C_1$ 
    - $(n-k_2) \times n$  Parity check matrix  $H_2$
    - $k_2 < k_1, C_2 \subset C_1$
- Equivalent relation
  - $u$  and  $v (u, v \in C_1)$  are "equivalent" iff there is a  $w \in C_2$  such that  $u = v + w$
- CSS code:
  - $k = k_1 - k_2$  quantum code
  - a codeword = each equivalent class

$$|\bar{v}\rangle = \frac{1}{\sqrt{2^{k_2}}} \sum_{w \in C_2} |w + v\rangle \quad v \in C_1, \notin C_2$$

$$2^{k_2} 2^{k_1 - k_2}$$

### The 7-qubit (Steane) code

- Hamming code:  $C_1 [7,4]$ 
  - Codewords  $u$ : spanned by the columns of  $G_1$
- Subcode:  $C_2 [7,3]$ 
  - Dual code  $C_1^\perp$ 
    - Generator  $H^T$ , Parity check matrix  $G^T$
  - Codewords  $w$ : spanned by the columns of  $G_2 (=H_1^T)$
- CSS code ( $k_1 = 4, k_2 = 3, k = k_1 - k_2 = 1$ )
  - Codeword = each equivalent class

$$|\bar{v}\rangle = \frac{1}{\sqrt{2^{k_2}}} \sum_{w \in C_2} |w + v\rangle \quad v \in C_1, \notin C_2$$

$$2^{k_2} 2^{k_1 - k_2}$$

### Codewords

- CSS code ( $k_1 = 4, k_2 = 3, k = k_1 - k_2 = 1$ )

$$|\bar{v}\rangle = \frac{1}{\sqrt{2^{k_2}}} \sum_{w \in C_2} |w + v\rangle \quad v \in C_1, \notin C_2$$

$$|\bar{0}\rangle = \frac{1}{\sqrt{2^{k_2}}} \sum_{w \in C_2} |w + 0000000\rangle, |\bar{1}\rangle = \frac{1}{\sqrt{2^{k_2}}} \sum_{w \in C_2} |w + 1111111\rangle$$

$$|\bar{0}\rangle = \frac{1}{\sqrt{8}} [ |0000000\rangle + |0001111\rangle + |0110011\rangle + |1010101\rangle + |0111100\rangle + |1100110\rangle + |1011010\rangle + |1101001\rangle ] = \frac{1}{\sqrt{8}} \sum_{w \in \text{even Hamming}} w$$

$$|\bar{1}\rangle = \frac{1}{\sqrt{8}} [ |1111111\rangle + |1110000\rangle + |1001100\rangle + |0101010\rangle + |1000011\rangle + |0011001\rangle + |0100101\rangle + |0010110\rangle ] = \frac{1}{\sqrt{8}} \sum_{w \in \text{odd Hamming}} w$$

### Bit flip error detection by parity check

- Bit flip errors
- Error syndrome = each column of  $H$

$$H(x_i|w) = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 1 \end{bmatrix}, H(x_2|w) = \begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \end{bmatrix}, H(x_3|w) = \begin{bmatrix} 0 \\ 1 \\ 1 \\ 1 \end{bmatrix}, \dots$$

$$H = \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 \end{bmatrix}$$

### Phase flip error detection by parity check

- Phase flip errors
  - Change basis
  - Bit flip errors

$$H = \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 \end{bmatrix}$$

### Logic operations on encoded qubits

### Universal quantum gates

1. Single qubit gates + C-NOT (exact)
2. Hadamard + phase (S) + C-NOT +  $\pi/8$  gates (T)

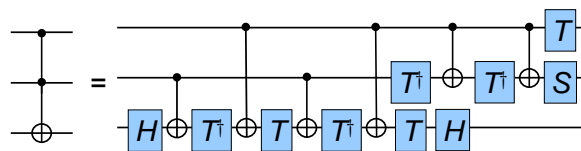
$$S = \begin{pmatrix} 1 & 0 \\ 0 & i \end{pmatrix}, T = \begin{pmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{pmatrix} = e^{i\pi/8} R_z\left(\frac{\pi}{4}\right)$$

- Approximate, since the set of unitary operations is continuous
- Error:  $E(U, V) \equiv \max_{|\psi\rangle} \|(U - V)|\psi\rangle\| < \varepsilon$ 
  - U: Target unitary operator
  - V: Unitary operator implemented

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### Construction of Toffoli gate

- Using Hadamard, Phase, C-NOT,  $\pi/8$  gates (Universal gate set)



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